

			Subclass
			ISSUE CLASSIFICATION

PATENT NUMBER

U.S. UTILITY Patent Application

④	O.T.P.E.	PATENT DATE
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SCANNED AA	Q.A. ne	

APPLICATION NO. 09/497533	CONT/PRIOR YES	CLASS 710 712	SUBCLASS ² 215	ART UNIT 2781 2409	EXAMINER Harkness
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APPLICANTS

Samuel Naffziger

2183

TITLE

A Find-instructions-and-allocate-ports (FIAP) circuit and method for an out of order processor

PTO-2040
12/89

ISSUING CLASSIFICATION									
ORIGINAL				CROSS REFERENCE(S)					
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION									

☐ Continued on Issue Slip Inside File Jacket.

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No: _____ _____ _____	_____ (Primary Examiner) (Date)		ISSUE FEE	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	

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